

**ESD protected N-Channel Enhancement Mode MOSFET**

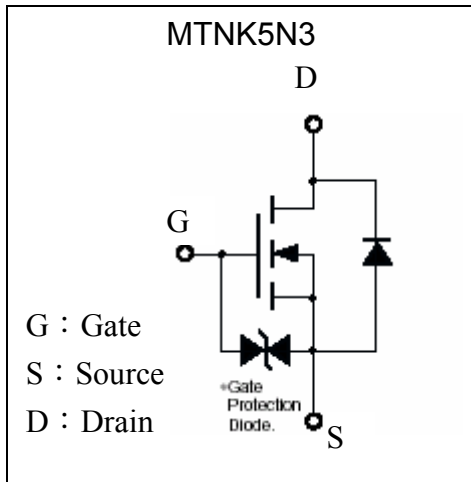
# MTNK5N3

BV <sub>DSS</sub>	30V
I <sub>D</sub>	100mA
R <sub>DS(on)(MAX)</sub>	8Ω

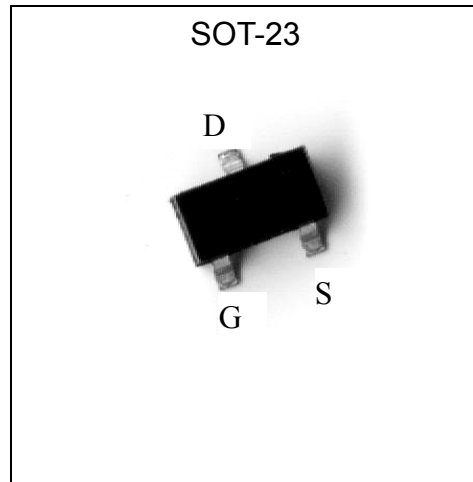
**Description**

- Low voltage drive(2.5V drive) makes this device ideal for portable equipment.
- High speed switching
- ESD protected device
- Pb-free lead plating & halogen-free package

**Symbol**

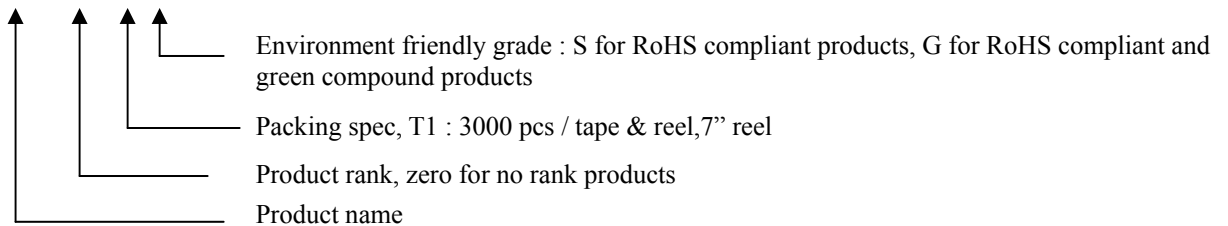


**Outline**



**Ordering Information**

Device	Package	Shipping
MTNK5N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





**Absolute Maximum Ratings (Ta=25°C)**

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	BV <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	±100	mA
Pulsed Drain Current (Ta=25°C)	I <sub>DM</sub>	±400 *1	mA
Total Power Dissipation	P <sub>D</sub>	300 *2	mW
ESD susceptibility		750 *3	V
Operating Junction and Storage Temperature Range	T <sub>j</sub> ; T <sub>stg</sub>	-55~+150	°C
Thermal Resistance, Junction-to-Ambient	R <sub>th,ja</sub>	556	°C/W

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max	R <sub>th,ja</sub>	417 *2	°C/W

- Note: \*1. Pulse Width ≤ 10μs, Duty cycle ≤ 1%  
 \*2. When device mounted on a FR-4 board with minimum pad size.  
 \*3. Human body model, 1.5kΩ in series with 100pF

**Electrical Characteristics (Ta=25°C)**

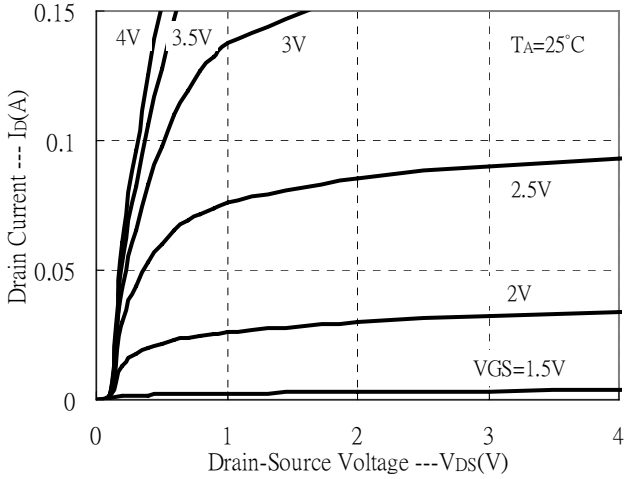
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =100μA
V <sub>GS(th)</sub>	0.8	1.3	1.5	V	V <sub>DS</sub> =3V, I <sub>D</sub> =100μA
I <sub>GSS</sub>	-	-	±1	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	100	nA	V <sub>DS</sub> =30V, V <sub>GS</sub> =0
R <sub>DSON</sub>	-	3.4	8	Ω	V <sub>GS</sub> =4V, I <sub>D</sub> =10mA
	-	6.9	13		V <sub>GS</sub> =2.5V, I <sub>D</sub> =1mA
G <sub>FS</sub>	20	50	-	mS	V <sub>DS</sub> =3V, I <sub>D</sub> =10mA
<b>Dynamic</b>					
C <sub>iss</sub>	-	12.5	-	pF	V <sub>DS</sub> =5V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	7.3	-		
C <sub>rss</sub>	-	3.5	-		
t <sub>d(on)</sub>	-	15	-	ns	V <sub>DD</sub> ≐ 5V, I <sub>D</sub> =10mA, V <sub>GS</sub> =5V, R <sub>L</sub> =500Ω, R <sub>G</sub> =10Ω
t <sub>r</sub>	-	35	-		
t <sub>d(off)</sub>	-	75	-		
t <sub>f</sub>	-	75	-		
<b>Source-Drain Diode</b>					
*V <sub>SD</sub>	-	0.88	1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =100mA

\*Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

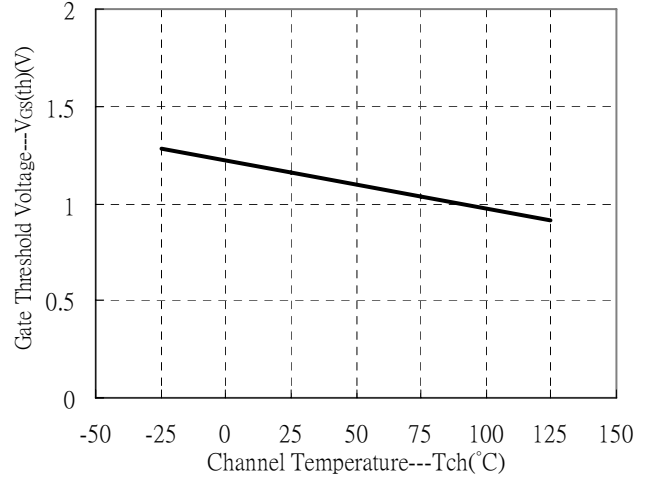


**Typical Characteristics**

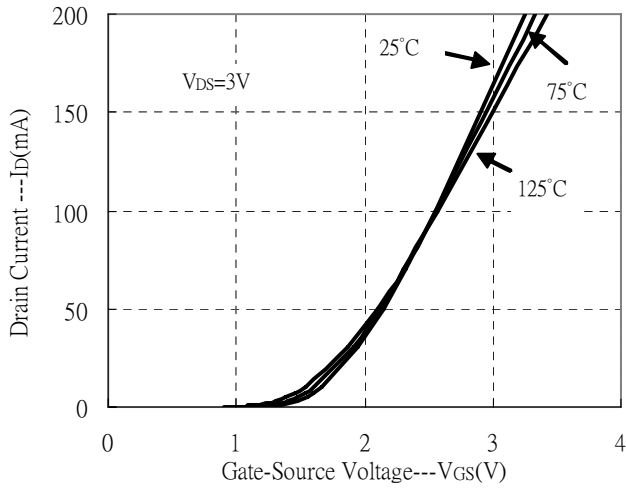
Typical Output Characteristics



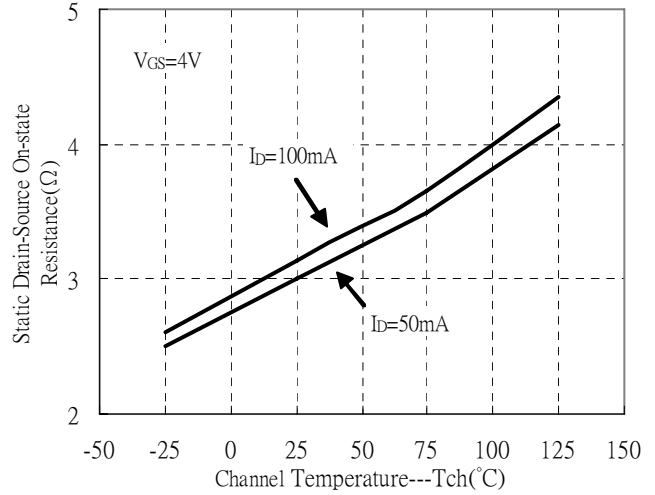
Gate Threshold Voltage vs Channel Temperature



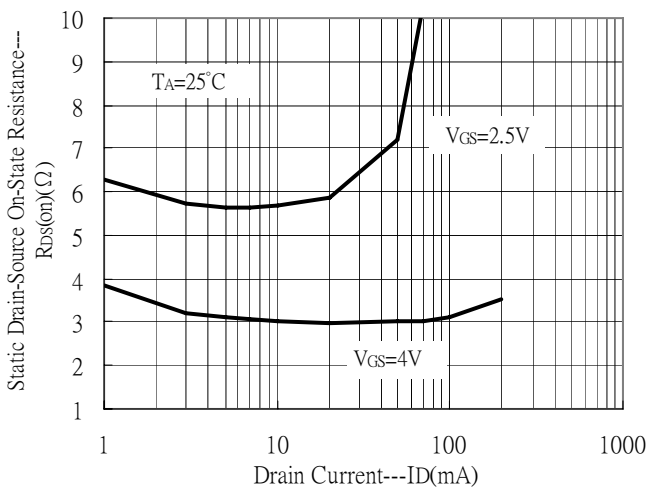
Typical Transfer Characteristics



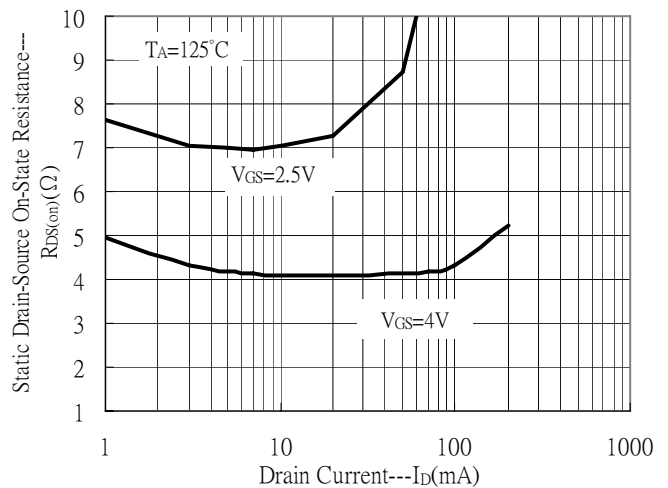
Static Drain-Source On-state Resistance with Temperature



Static Drain-Source On-State resistance vs Drain Current

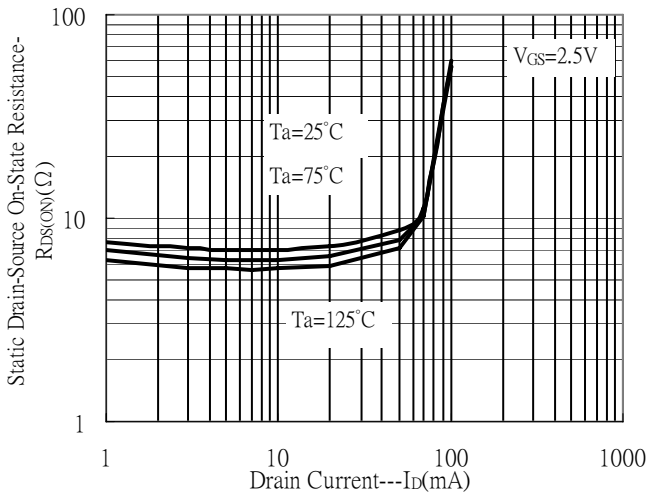


Static Drain-Source On-State resistance vs Drain Current

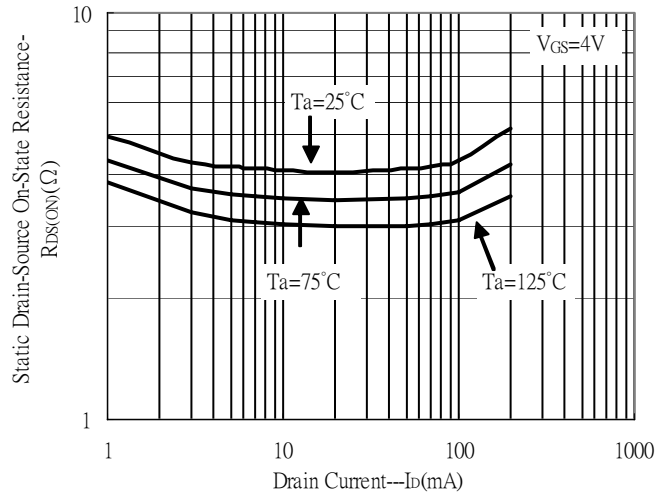


**Typical Characteristics(Cont.)**

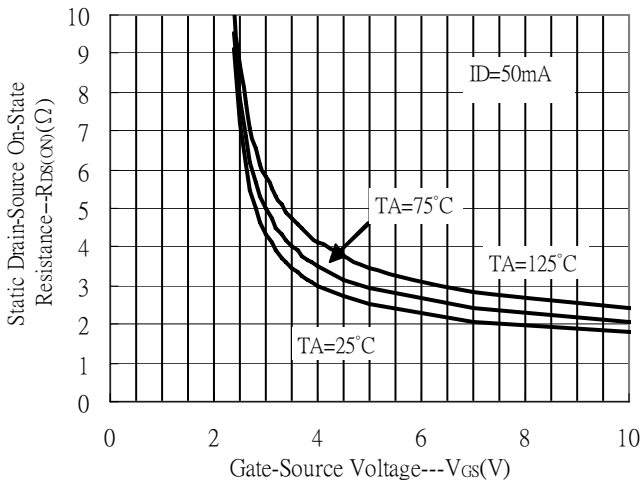
Static Drain-Source On-State Resistance vs Drain Current



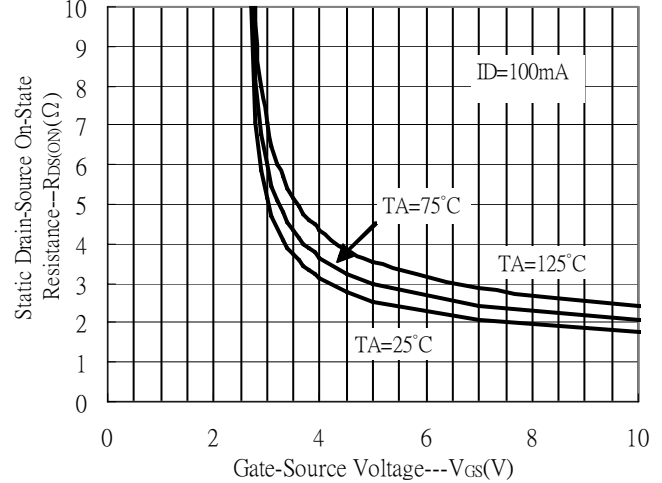
Static Drain-Source On-State Resistance vs Drain Current



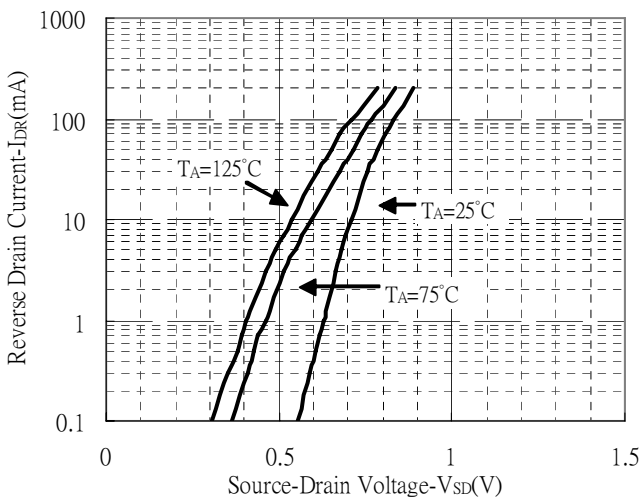
Static Drain-Source On-State Resistance vs Gate-Source Voltage



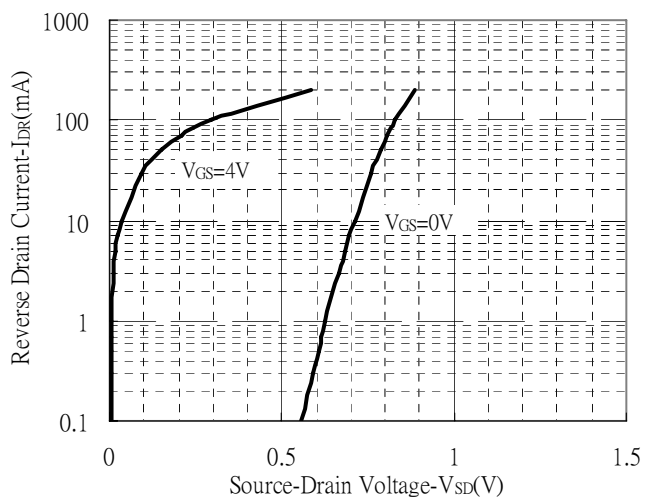
Static Drain-Source On-State Resistance vs Gate-Source Voltage



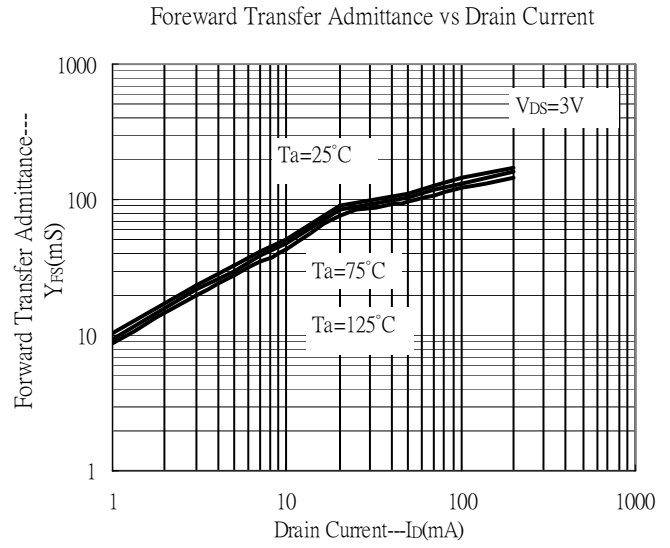
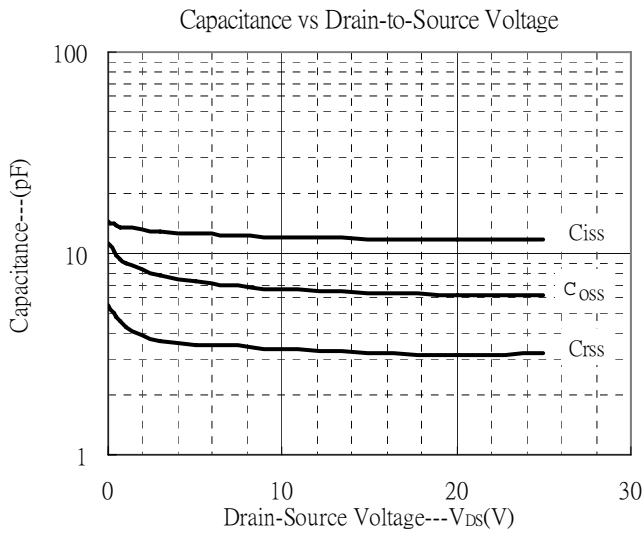
Reverse Drain Current vs Source-Drain Voltage(I)



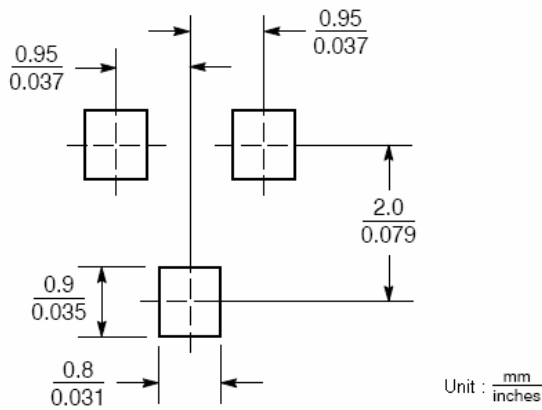
Reverse Drain Current vs Source-Drain Voltage(II)



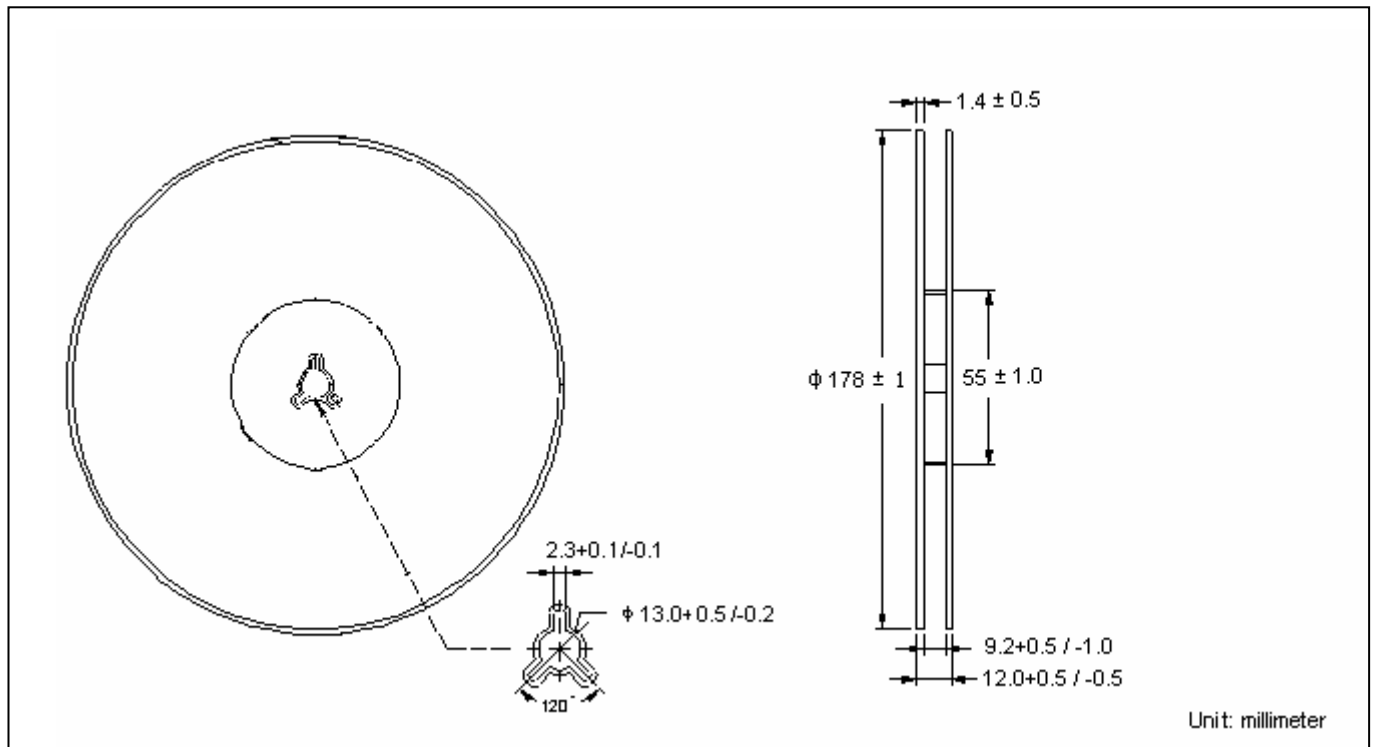
**Typical Characteristics(Cont.)**



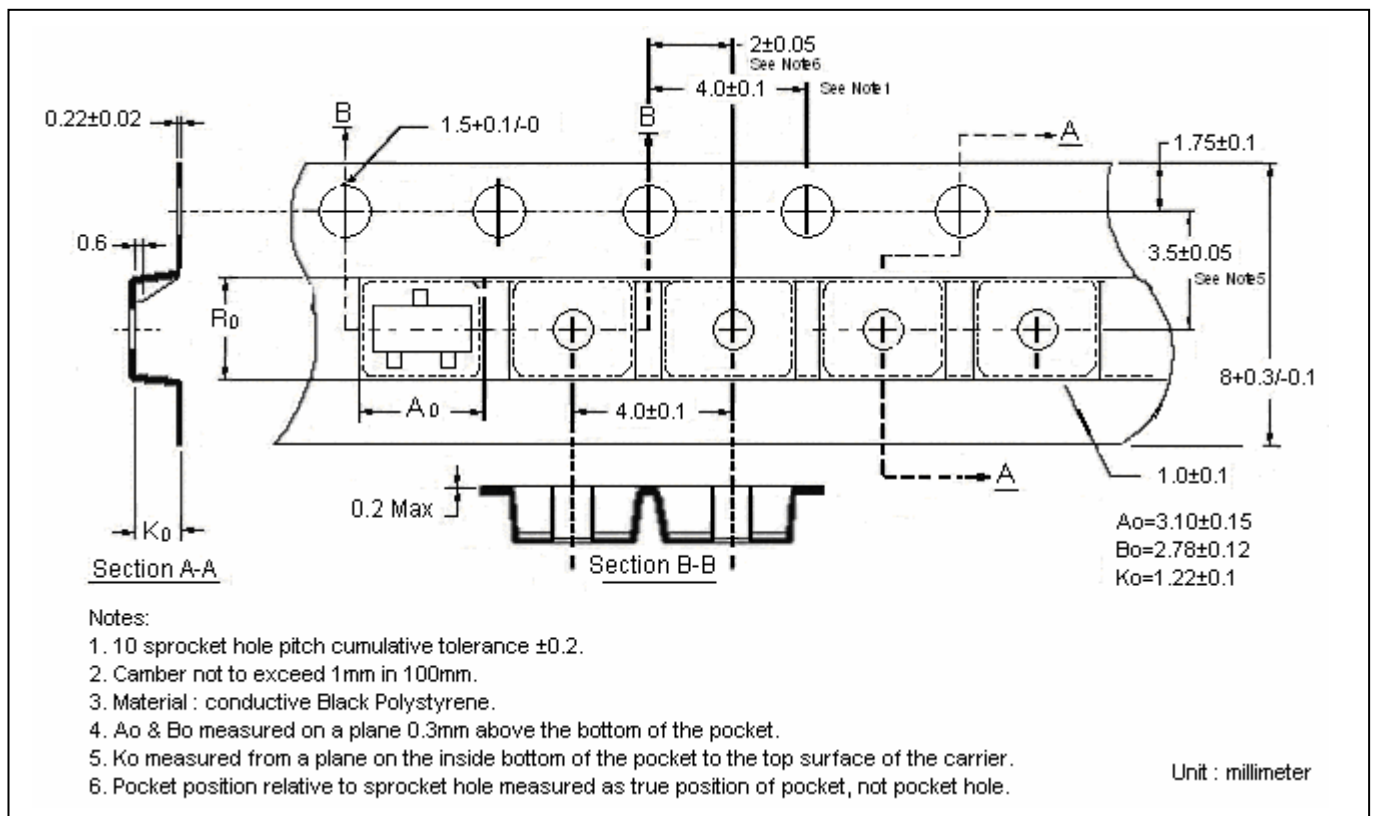
**Recommended Soldering Footprint**



**Reel Dimension**



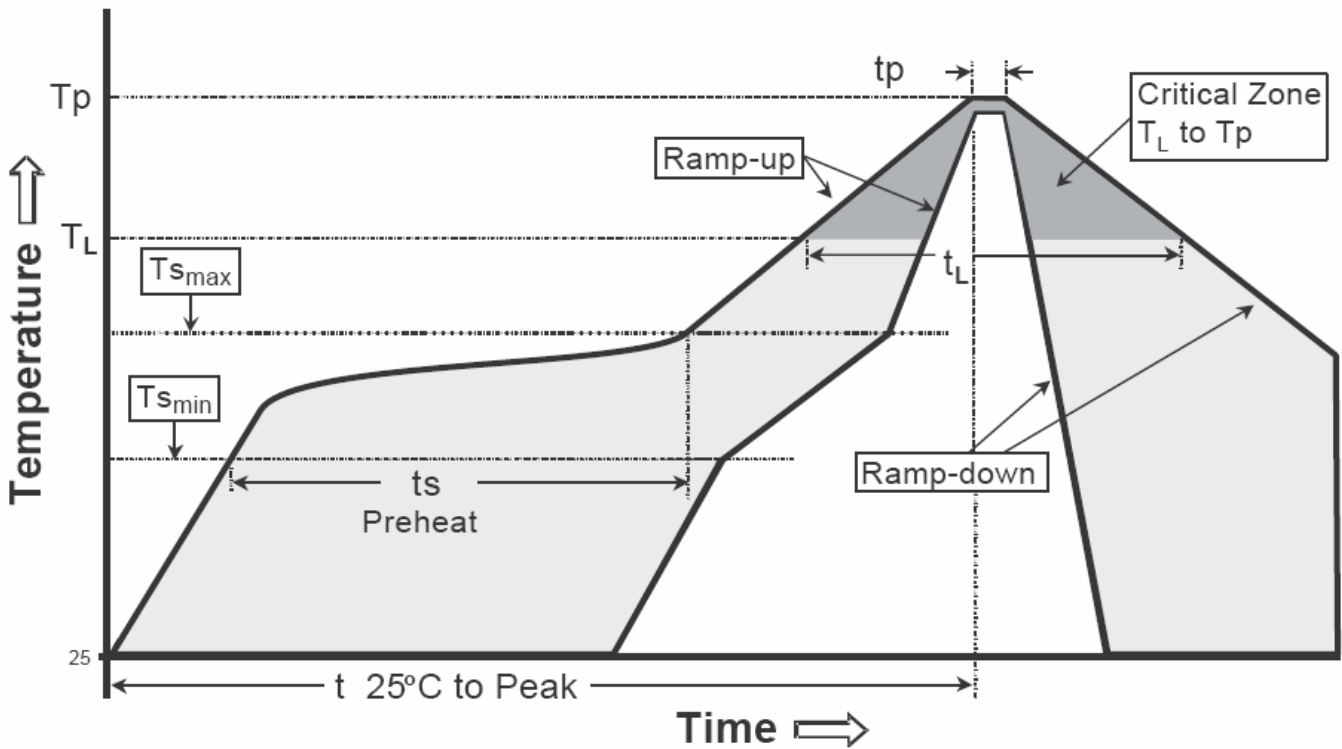
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

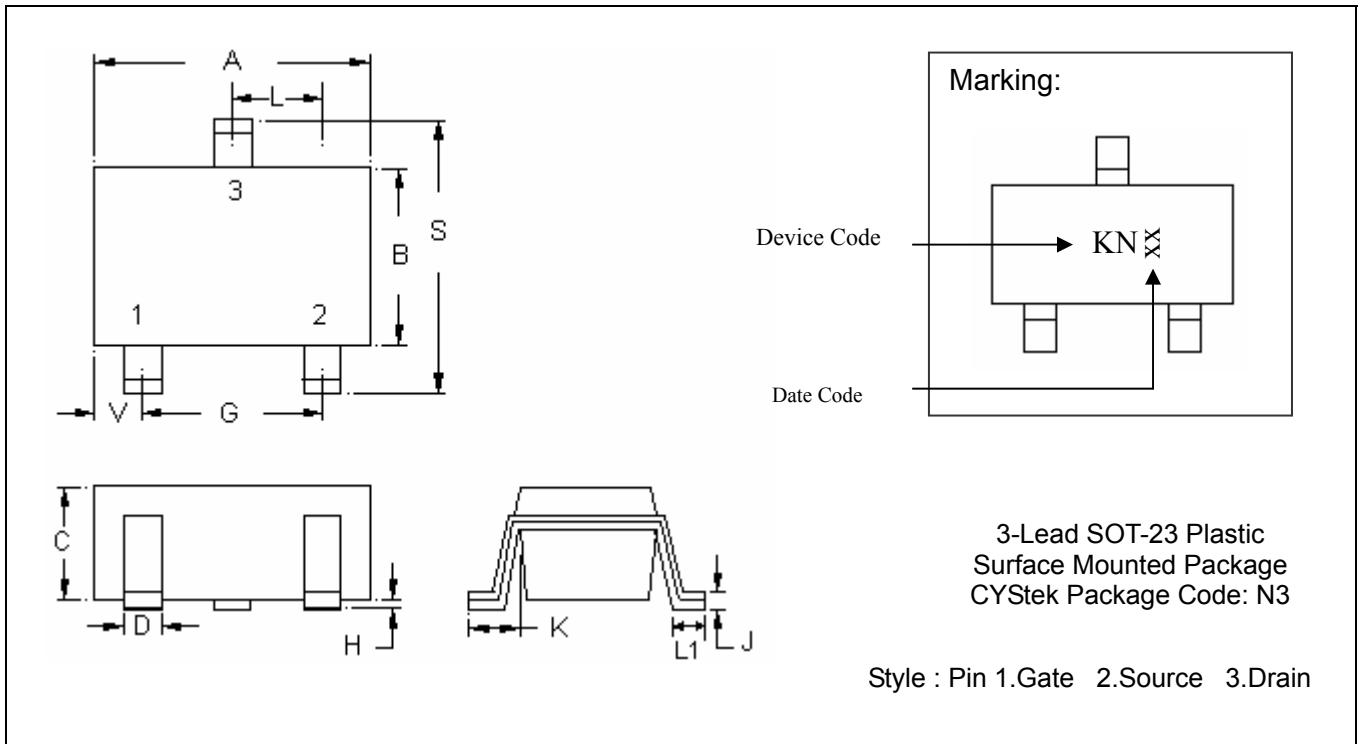
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**SOT-23 Dimension**



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50

Notes : 1.Controlling dimension : millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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